

30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB

General Description

The Sanrise SRC60R030FB is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R030FB break down voltage is 600V and it has a high rugged avalanche characteristics. The SRC60R030FB is available in TO-247 package.

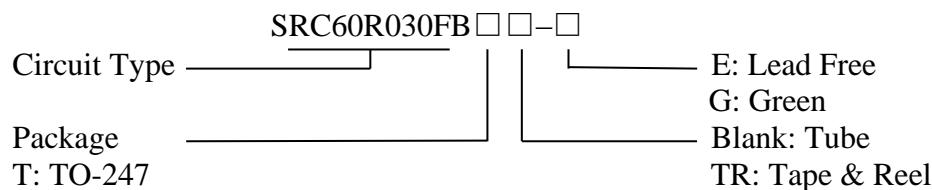
Features

- Ultra Low $R_{DS(ON)}$ = 30mΩ @ V_{GS} = 10V.
- Ultra Low Gate Charge, Q_g =124.1nC typ.
- Fast switching capability
- Robust design with better EAS performance
- Low Qrr

Application

- EV Charger
- High Power Application
- High Performance Application

Ordering Information



Symbol

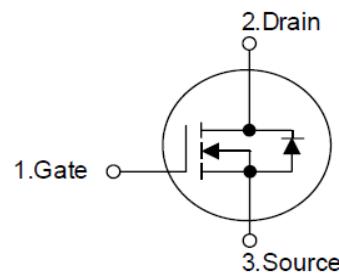


Figure 1 Symbol of SRC60R030FB

Package Type



TO-247

Figure 2 Package Type of SRC60R030FB

Package	Part Number		Marking ID		Packing Type
	Lead Free	Green	Lead Free	Green	
TO-247	SRC60R030FBT-E	SRC60R030FBT-G	SRC60R030FBTE	SRC60R030FBTG	Tube

30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB
Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	600	V
Gate-Source Voltage (static)	V _{GSS}	±20	V
Gate-Source Voltage (dynamic), AC (f>1 Hz)	V _{GSS}	±30	V
Continuous Drain Current	I _D	91	A
T _C =125°C		41	
Pulsed Drain Current (Note 2)	I _{DM}	273	A
Avalanche Energy, Single Pulse (Note 3)	E _{AS}	1200	mJ
Avalanche Energy, Repetitive (Note 2)	E _{AR}	1.0	mJ
Avalanche Current, Repetitive (Note 2)	I _{AR}	7.5	A
Continuous Diode Forward Current	I _S	91	A
Diode Pulse Current	I _{S,PULSE}	273	A
MOSFET dv/dt Ruggedness, V _{DS} <=480V	dv/dt	80	V/ns
Reverse Diode dv/dt, V _{DS} <=480V, I _{SD} <=I _D	dv/dt	50	V/ns
Power Dissipation	P _{tot}	595	W
Operating Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	T _{LEAD}	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I_{AS}=7.5A, V_{DD} = 60V, R_G = 25Ω, Starting T_J = 25°C

Thermal characteristics

Parameter	Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	R _{thJC}			0.21	°C /W
Thermal resistance, Junction-to-Ambient	R _{thJA}			54	°C /W

30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB
Electrical Characteristics
 $T_J = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$			10	μA
Gate-Body Leakage Current	Forward	I_{GSSF}	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA
	Reverse	I_{GSSR}	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100	
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=2.2\text{mA}$	3.0	4.0	5.0	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=34\text{A}$		26.4	30	$\text{m}\Omega$
Gate Resistance	R_G	f=1MHz, Open Drain		2.0		Ω
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		8.3		nF
Output Capacitance	C_{OSS}			393		pF
Reverse Transfer Capacitance	C_{RSS}			2.7		pF
Effective output capacitance, energy related ^{NOTE5}	$\text{C}_{\text{O(er)}}$	$\text{V}_{\text{GS}}=0\text{V},$ $\text{V}_{\text{DS}}=0\ldots 400\text{V}$		222.		pF
Effective output capacitance, time related ^{NOTE6}	$\text{C}_{\text{O(tr)}}$			4		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=400\text{V}, \text{I}_D=34\text{A}$ $\text{R}_G=3.3\Omega, \text{V}_{\text{GS}}=10\text{V}$		21		ns
Rise Time	t_r			13		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			71		
Fall Time	t_f			4.5		
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$\text{V}_{\text{DD}}=480\text{V}, \text{I}_D=34\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$		58.5		nC
Gate to Drain Charge	Q_{gd}			33.7		
Gate Charge Total	Q_g			124.		
Gate Plateau Voltage	$\text{V}_{\text{plateau}}$			1		
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{SD}}=34\text{A}$		0.9	1.1	V
Reverse Recovery Time	t_{rr}	$\text{V}_{\text{R}}=400\text{V}, \text{I}_{\text{F}}=34\text{A}$ $d\text{I}_{\text{F}}/dt=100\text{A}/\mu\text{s}$		175		ns
Reverse Recovery Charge	Q_{rr}			1.28		uC
Peak Reverse Recovery Current	I_{rrm}			14.6		A

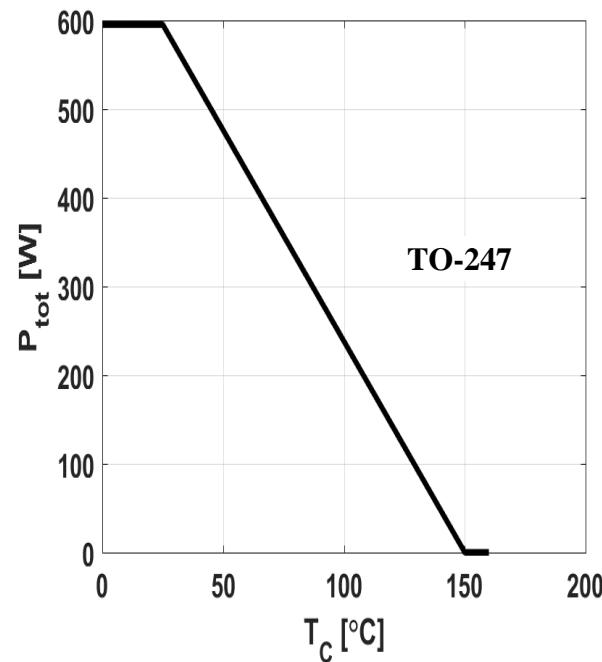
Note:

5. $\text{C}_{\text{O(er)}}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V

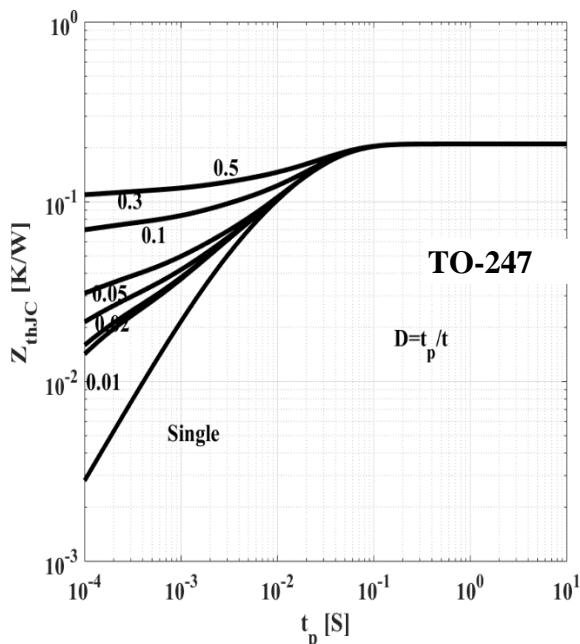
6. $\text{C}_{\text{O(tr)}}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480 V

30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB

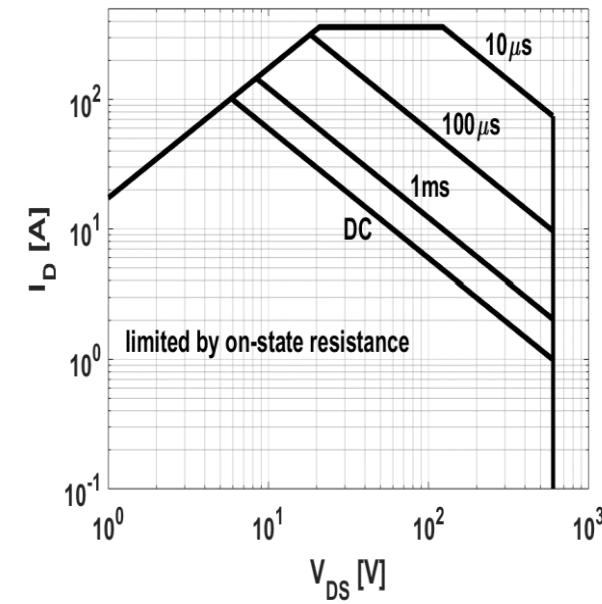
Typical Performance Characteristics

Figure 3: Power Dissipation


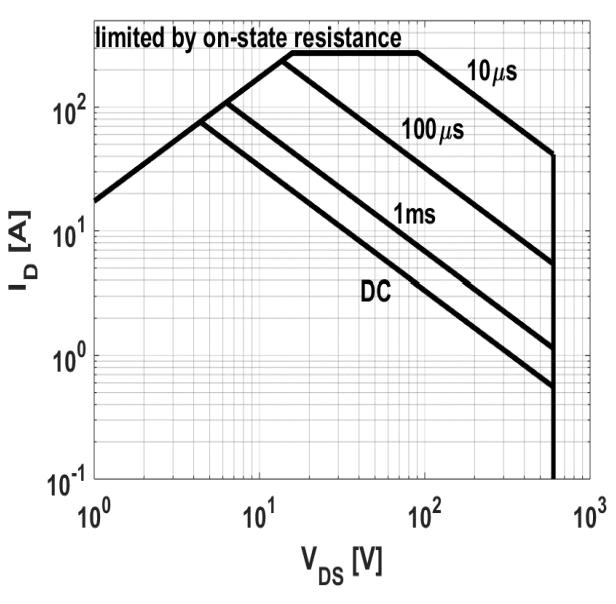
$$P_{\text{tot}} = f(T_c)$$

Figure 4: Max. Transient Thermal Impedance


$$Z_{\text{thJC}} = f(t_p); \text{ parameter: } D = t_p/T$$

Figure 5: Safe Operating Area


$$I_D = f(V_{DS}); T_c = 25^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

Figure 6: Safe Operating Area


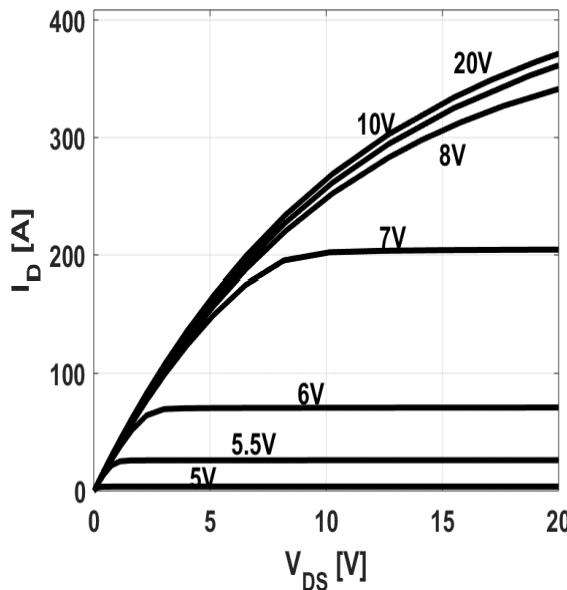
$$I_D = f(V_{DS}); T_c = 80^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$



30mΩ, 600V, Super Junction N-Channel Power MOSFET

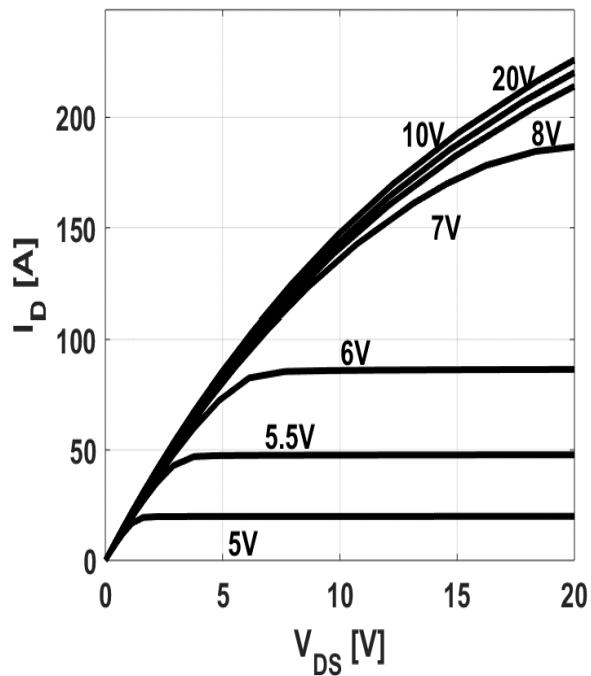
SRC60R030FB

Figure 7: Typ. Output Characteristics



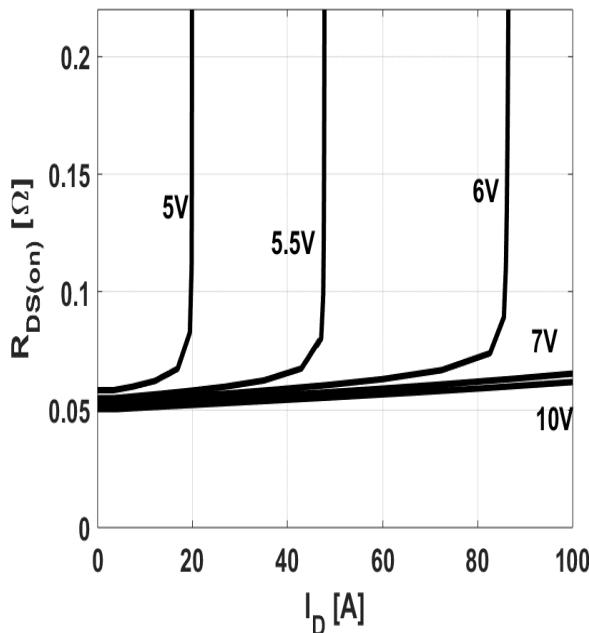
$I_D = f(V_{DS})$; $T_j = 25^\circ\text{C}$; parameter: V_{GS}

Figure 8: Typ. Output Characteristics



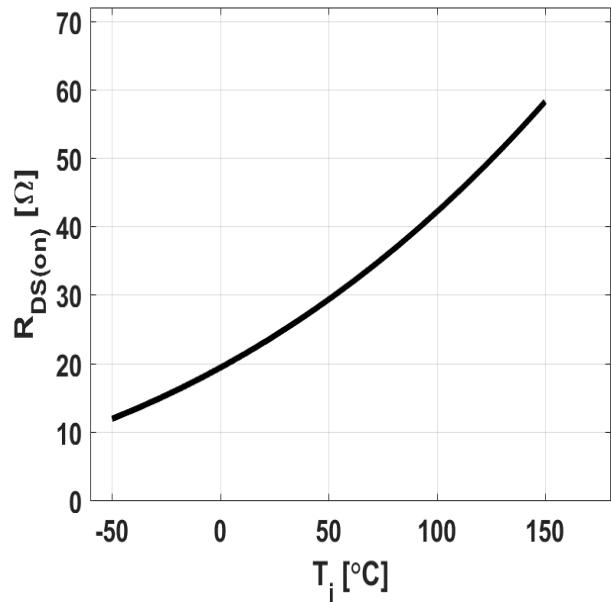
$I_D = f(V_{DS})$; $T_j = 125^\circ\text{C}$; parameter: V_{GS}

Figure 9: Typ. Drain-Source On-State Resistance



$R_{DS(ON)} = f(I_D)$; $T_j = 125^\circ\text{C}$; parameter: V_{GS}

Figure 10: Typ. Drain-Source On-State Resistance



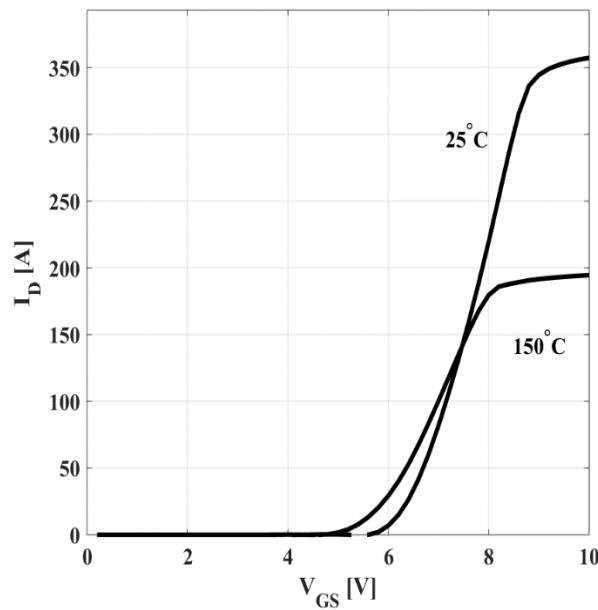
$R_{DS(ON)} = f(T_j)$; $I_D = 34\text{A}$; $V_{GS} = 10\text{V}$



30mΩ, 600V, Super Junction N-Channel Power MOSFET

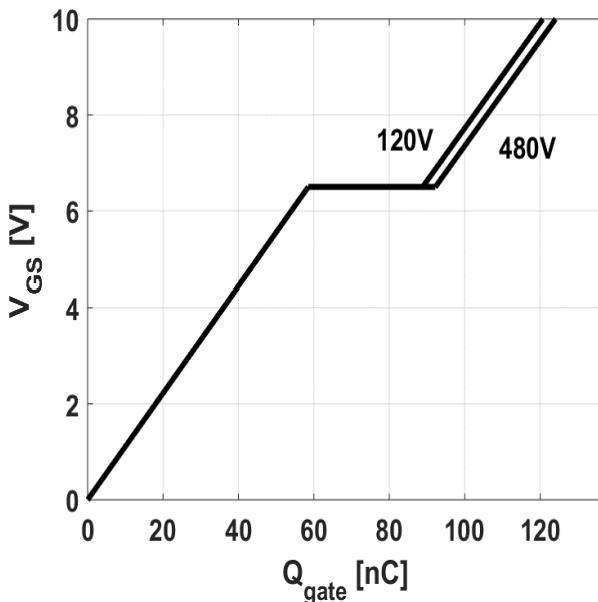
SRC60R030FB

Figure 11: Typ. Transfer Characteristics



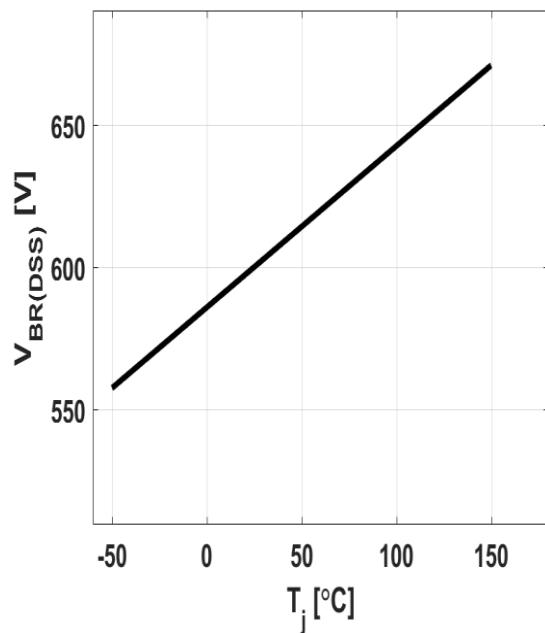
$$I_D = f(V_{GS}); V_{DS} = 20V$$

Figure 12: Typ. Gate Charge



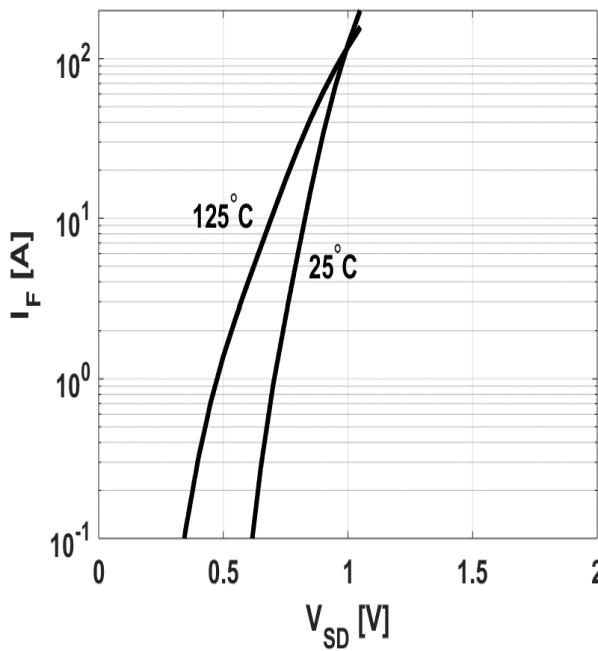
$$V_{GS} = f(Q_{gate}), I_D = 34A \text{ pulsed}$$

Figure 13: Drain-Source Breakdown Voltage



$$V_{BR(DSS)} = f(T_j); I_D = 10mA$$

Figure 14: Forward Characteristics of Reverse Diode



$$I_F = f(V_{SD}); \text{ parameter: } T_j$$

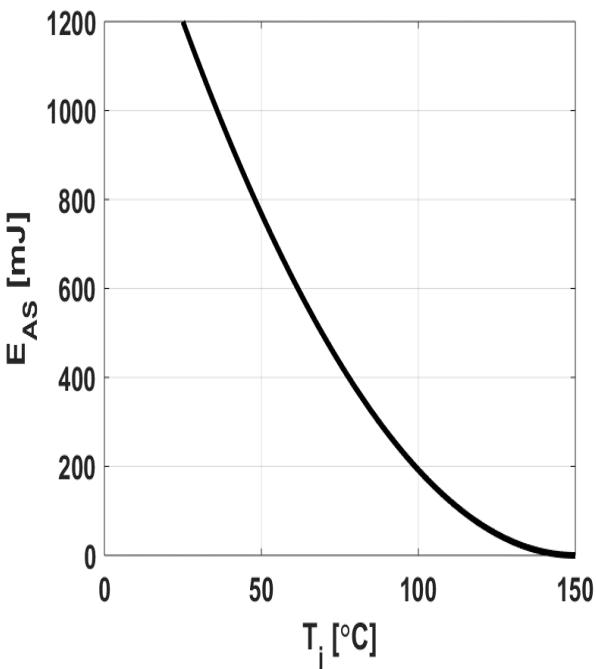
30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB
Figure 15: Avalanche Energy

E_{AS}=f(T_j); I_D=7.5A; V_{DD}=60V

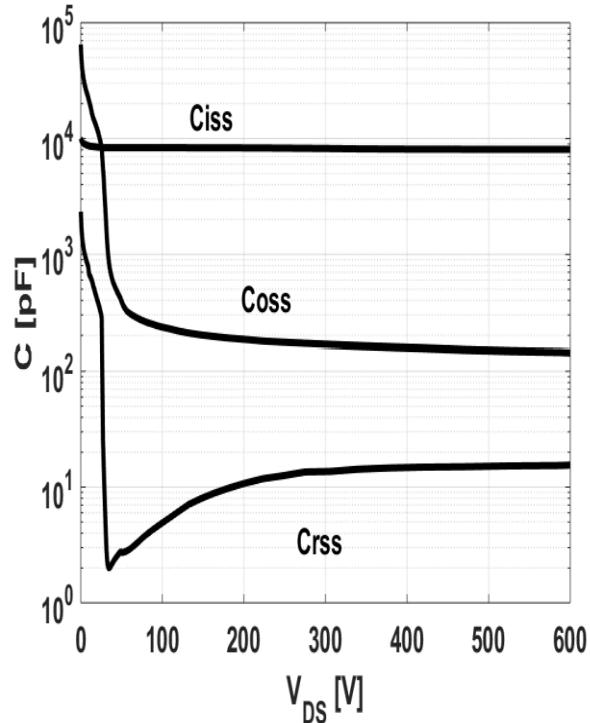
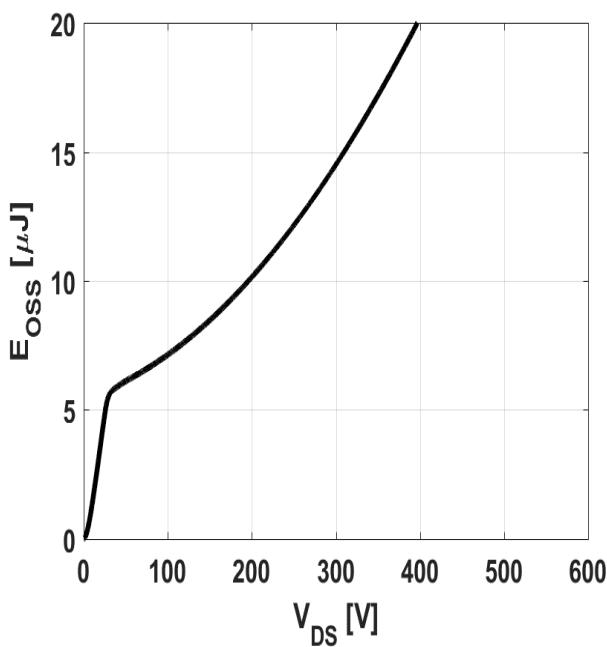
Figure 16: Typ. Capacitances

C=f(V_{DS}); V_{GS}=0; f=1MHz

Figure 17: Coss Stored Energy

E_{oss}=f(V_{DS})

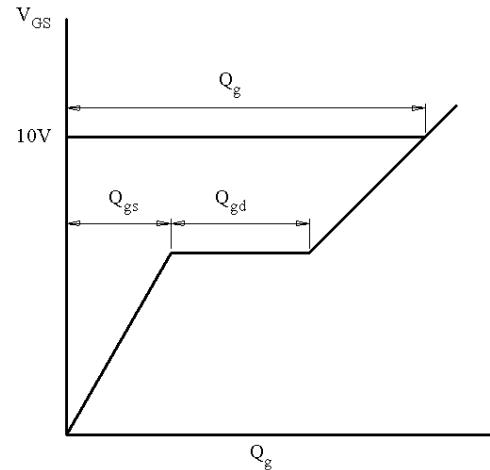
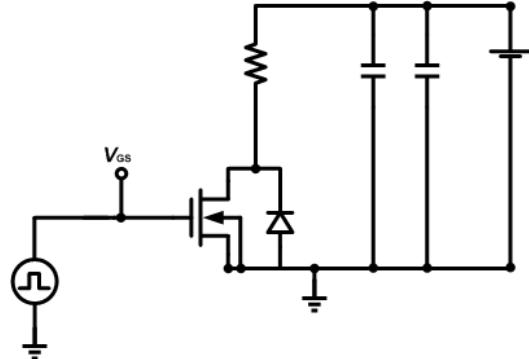


30mΩ, 600V, Super Junction N-Channel Power MOSFET

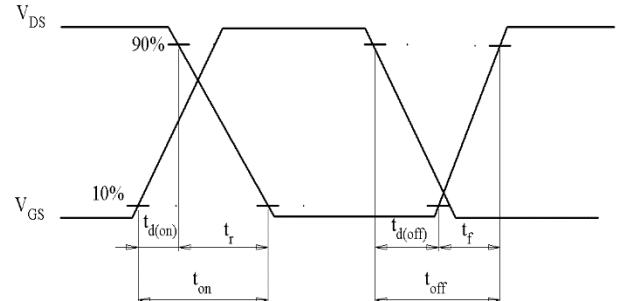
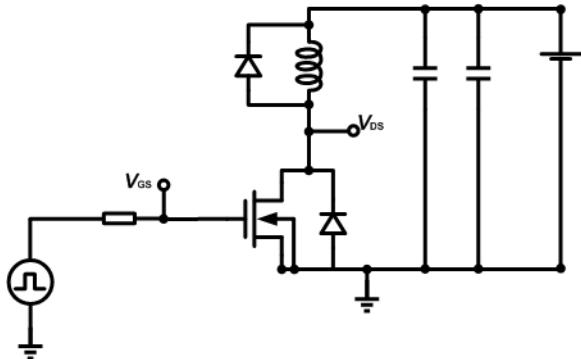
SRC60R030FB

Test Circuits

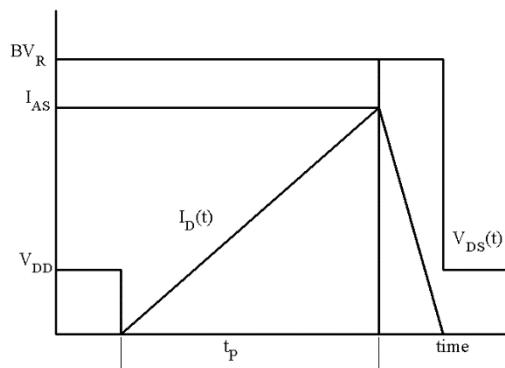
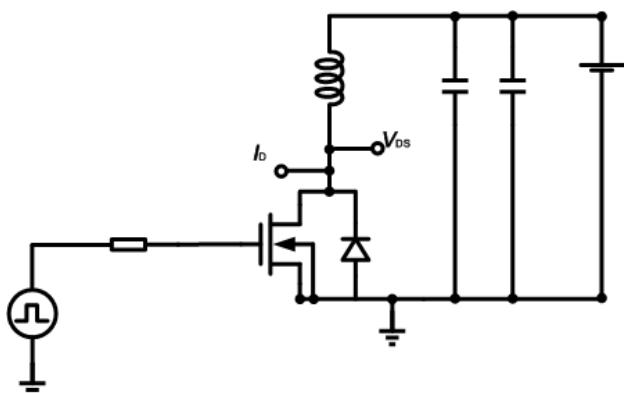
1. Gate Charge Test Circuit & Waveform



2. Switch Time Test Circuit

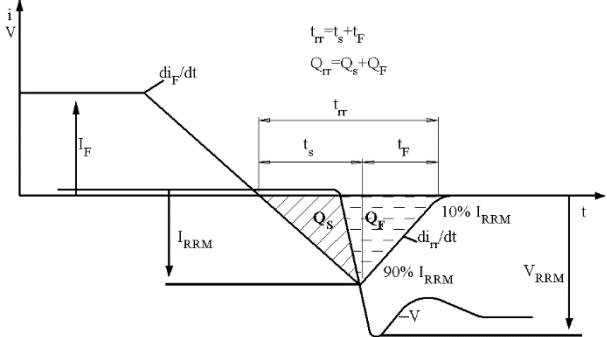
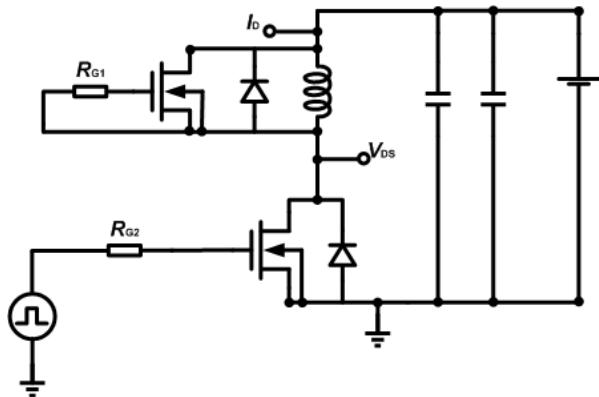


3. Unclaimed Inductive Switching Test Circuit & Waveforms



30mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R030FB

4. Test Circuit and Waveform for Diode Characteristics





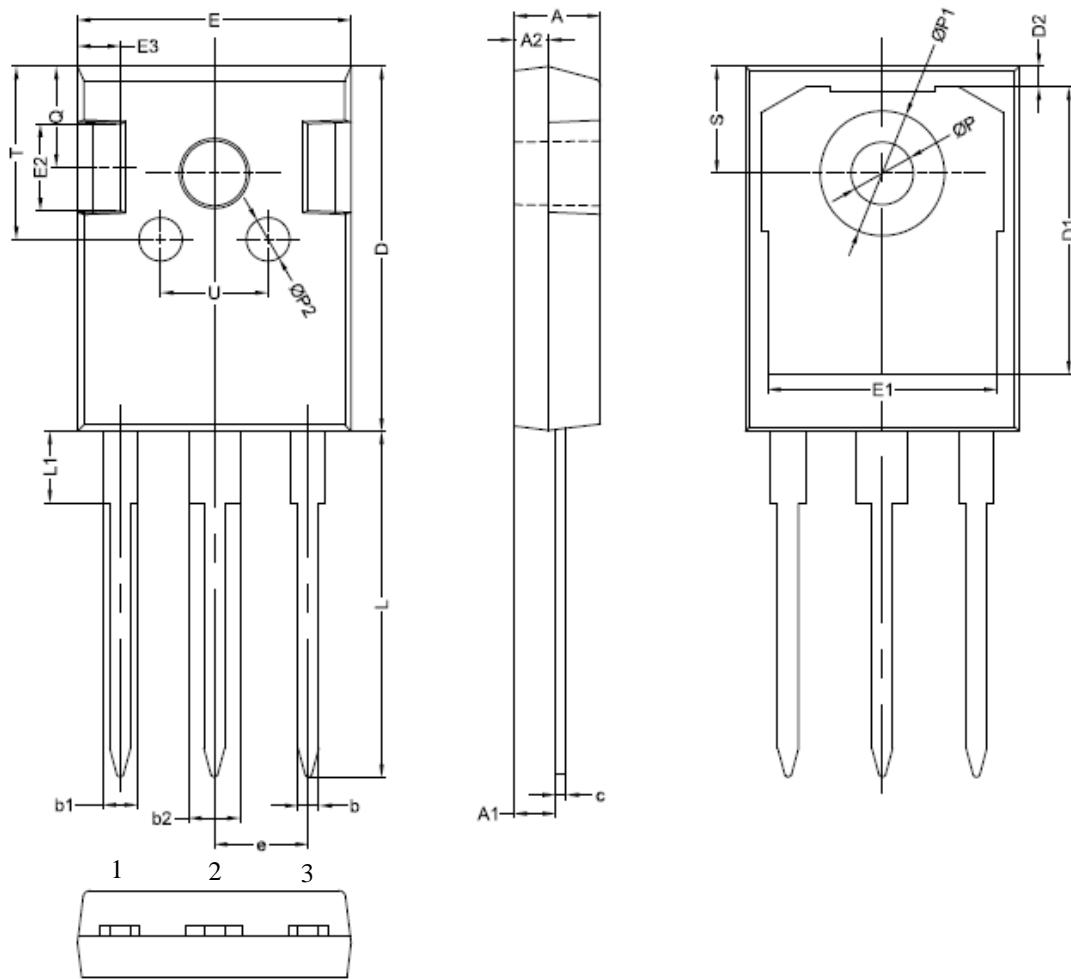
30mΩ, 600V, Super Junction N-Channel Power MOSFET

SRC60R030FB

Mechanical Dimensions

TO-247

Unit: mm



Symbol	Dimensions(mm)			Symbol	Dimensions(mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.80	5.00	5.20	E2	-	5.00	-
A1	2.21	2.41	2.61	E3	-	2.50	-
A2	1.90	2.00	2.10	e	5.44(BSC)		
b	1.10	1.20	1.35	L	19.42	19.92	20.42
b1	-	2.00	-	L1	-	4.13	-
b2	-	3.00	-	P	3.50	3.60	3.70
c	0.55	0.60	0.75	P1	-	-	7.40
D	20.80	21.00	21.20	P2	-	2.50	-
D1	-	16.55	-	Q	-	5.80	-
D2	-	1.20	-	S	6.05	6.15	6.25
E	15.60	15.80	16.00	T	-	10.00	-
E1	-	13.30	-	U	-	6.20	-



Shenzhen Sanrise Technology Co., LTD

<http://www.sanrise-tech.com>

IMPORTANT NOTICE

Shenzhen Sanrise Technology Co., LTD reserves the right to make changes without further notice to any products or specifications herein. Shenzhen Sanrise Technology Co., LTD does not assume any responsibility for use of any its products for any particular purpose, nor does Shenzhen Sanrise Technology Co., LTD assume any liability arising out of the application or use of any its products or circuits. Shenzhen Sanrise Technology Co., LTD does not convey any license under its patent rights or other rights nor the rights of others.

Main Site:**- Headquarter**

Shenzhen Sanrise Technology Co., LTD.

A1206, Skyworth building, No. 008, gaoxinnan 1st Road,
Gaoxin District, Yuehai street,, Nanshan District, ShenZhen,
P.R.China

Tel: +86-755-22953335

Fax: +86-755-22916878

- Shanghai Office

Shenzhen Sanrise Technology Co., LTD

Rm.702, Building A, No. 666, Zhangheng Road,
Zhangjiang Hi-Tech Park, Shanghai, P.R.China

Tel: +86-21-68825918